



Republic of the Philippines

Department of Science and Technology

ADVANCED SCIENCE AND TECHNOLOGY INSTITUTE



ASTI-FM 03-10
REV 2 / 13 MAR 2019

**DOST-ASTI Bids and Awards Committee
REQUEST FOR QUOTATION**

Kind of Procurement Activity:	Negotiated Procurement:Small-value Procurement		
Deadline of Submission of Bids:	Oct-08-2019, 2:00 PM		
RFQ No.:	19-10-2810	Date:	October-03-2019
PR No.:	GAA-19-09-8744	Date:	September-23-2019

The Department of Science and Technology (DOST) - Advanced Science and Technology Institute (ASTI), through its Bids and Awards Committee (BAC), intends to procure the item/s listed below. As such, suppliers, contractors, or distributors are invited to submit their quotation/s duly signed by authorized representative. Prospective bidder/s who will submit a proposal with the single/lowest calculated and responsive quotation shall be selected. Guidelines on the format and eligibility documents are listed at the box below the item/s to be procured.

Quotations may be sent via **a)** electronic mail at bac-sec@asti.dost.gov.ph, **b)** fax message, or **c)** delivering documents to the BAC Secretariat. For further inquiries, you may contact +63 2 249-8500 local 1206/1212 or +63 2 426-7423.

Thank you.

Respectfully,


PEDRITO B. MANGAHAS
Chairperson, BAC-1

NO.	TECHNICAL SPECIFICATIONS	QTY	UNIT	UNIT PRICE(Php)	TOTAL PRICE(Php)
1	<p>PICC Compiler</p> <ul style="list-style-type: none"> - IDE compiler must support Microchip PIC10/12/16/18/24/dsPIC Devices - IDE compiler's latest version - IDE must be highly optimized and feature-rich ANSI C compiler for Microchip PIC® MCUs - IDE should provide embedded developers with a suite of tools and an intelligent code optimizing - IDE must allow developers to manage every aspect of their embedded software development, from design through device programming and debugging - must have built-in functions, performance analyzation, and statistics, and debugging compiled code in real-time while running on Microchip PIC® MCU devices. - includes a comprehensive Help File to reference while you work without losing valuable time, free limited-time maintenance, and free tech-support for the life of the product - includes libraries for SPI, ADC, I2C, Timers, PWM, and more - accelerates device and application development - Route program I/O to a PC using an ICD - Jump-start and configure many application peripherals <p>Compiler Features:</p> <ul style="list-style-type: none"> - desianed speccificallv for the PIC® MCU architecture 	3	pc	40000.00	120,000.00

- must be optimized for the PIC® MCU
- must include Standard C Constructs, numerous Pre-Processor Functions, and the largest library of Built-In Functions
- the compiler must be ANSI C Compliant with Pro-Level Optimization
- should have flexible software configurations to fit any project requirements
- should be compatible with many third-party tools such as Microchip MPLAB® and MPLAB® X

Pro-Level Optimization

Device-specific includes files contain all the information the compiler needs to optimize code generation for the specific PIC® MCU.

- Instruction memory size
- Data memory size
- Pin functionality
- Memory banking
- Peripheral resources
- Hardware stack size

String Optimization

- decrease system usage through improved string compression
- easily perform string comparisons which result in tighter and more maintainable source code and a smaller ROM footprint
- reduce the usage of multiple string output through the use of this function

Efficient Data Structures Mapped into Program Memory

- must optimize for space instead of speed with up to 60% reduction of program memory
- allows for call trees to be deeper than the hardware stack
- practical handling of multiple code files
- insert assembly code anywhere in the source and reference C variables
- allows for several functions with the same name with differences in number and types of parameters
- define functions which allow for any number of variables
- permits the compiler to generate very efficient bit-oriented code
- Intelligent Interrupt Handling
- Automatic Fuse Configuration
- Function Recursion for PIC24 and dsPIC® DSC Devices
- Relocatable Objects / Multiple Compilation Unit (IDE Only)

Largest Library of Built-In Functions:

Device Peripheral Access:

- I2C and SPI
- A/D converters
- On-chip data EEPROM
- LCD controllers
- Processor controls
- Capacitive touch
- Memory Access

- Real-time Clock
 External Memory Access:
 - Create User-Defined Address Spaces
 - Generate Multiple HEX Files for Applications
 Advantages:
 - Connect to a number of busses quickly
 - Easily setup Capture/Compare/PWM Peripherals
 - Specify microcontroller clock speed
 - Generate precision delays in microseconds & milliseconds
 - Setup and read values from on-chip A/D converters
 - PLL and power saving options
 - Swiftly integrate Capacitive Touch into projects
 - Drive internal PIC® LCD controllers
 - Maintain tri-state registers
 - Map PIC® registers to C variables
 - Interface codec chips to dsPIC® DSCs

- includes drivers for LCD Modules, Keypads, Serial EEPROM, RTC, Touch Sensors, Memory Devices, A/D Converters, Temperature Sensors, Digital Potentiometers, I/O Expanders, etc.
 - includes standard C Math Libraries such as 1/8/16/32-bit Integers and 32-bit Floating Point Support
 - All Devices, 48/64-bit Integers, and 64-bit Floating Point Support - PIC24 and dsPIC® DSC Devices, etc
 - includes ready-to-run program examples

Note:
 - must be compatible with existing development tools used by ESG

TOTAL APPROVED BUDGET FOR THE CONTRACT:

Php 120,000.00

GUIDELINES

A. Submission of Quotations

1. Quotation/s shall include the Request for Quotation and/or the Purchase Request Number as state above;
2. Pictures or brand/model names or numbers, if applicable, should be specified in the quotation/s; and
3. Quotation/s must be signed by the company's duly authorized representative.

B. Eligibility Requirements

Pursuant to Annex "H" or Consolidated Guidelines for the Alternative Methods of Procurement of the 2016 Implementing Rules and Regulations (IRR) of Republic Act (RA) No. 9184, as amended by Government Procurement Policy Board Resolution No. 21-2017 dated 30 May 2017, the following documents shall be submitted except for Repeat Order, Shopping under Section 52.1(a), and Negotiated Procurement under Sections 53.1 (Two-Failed Biddings), and 53.5 (Agency-to-Agency):

For Procurement of Goods

1. Upon submission of quotation
 - ✓ PhilGEPs Platinum Membership Certificate including Annex "A". If not available, the following alternate documents may be submitted:
 - PhilGEPs Registration Number
 - Mayor's Permit
 - For individuals/professionals engaged under Section 53.6, 53.7 and 53.9 of the 2016 IRR of RA No. 9184, only the Bureau of Internal Revenue (BIR) Certificate of Registration shall be submitted in lieu of the Mayor's Permit.

2. Upon issuance of Notice of Award (NOA)

- ✓ Omnibus Sworn Statement
 - Applicable only for bidders who have submitted their quotation on item/s with a total Approved Budget for the Contract (ABC) of above Php50,000.00.
- ✓ Income/Business Tax Return
 - Applicable only for: **a)** bidders who have submitted their quotation on item/s with a total ABC of above Php500,000.00; and **b)** bidders for Lease of Real Property and Venue (except for government agencies as lessors).

For Procurement of Infrastructure

1. The requirements for Goods with the same submission indicated therein; and
2. Valid Philippine Contractors Accreditation Board License.

For Procurement of Consulting Services

1. The requirements for Goods with the same submission indicated therein; and
2. Valid Professional Regulation Commission License or Curriculum Vitae.

NOTE: For new suppliers, submit a BIR Certificate of Registration for accounting purposes.

C. Terms and Conditions

1. Additional requirements, if necessary, may be requested by the BAC depending on the item to be bid;
2. For all kinds of procurement, the bidder who passed the bid evaluation, shall submit a duly notarized Omnibus Sworn Statement upon issuance of NOA, unless otherwise provided;
3. All transactions are subject to creditable withholding tax and final Value Added Tax or percentage tax per revenue regulation/s of the BIR;
4. A penalty of one-tenth of one percent (0.001) of the total value of the undelivered goods/services shall be charged as liquidated damages for every day of delay of the delivery; and
5. The DOST-ASTI reserves the right to accept or reject any proposal, to annul the bidding process, and to reject all proposals at any time prior to contract award, without thereby incurring any liability to the affected proponent or proponents.